FIG. 1

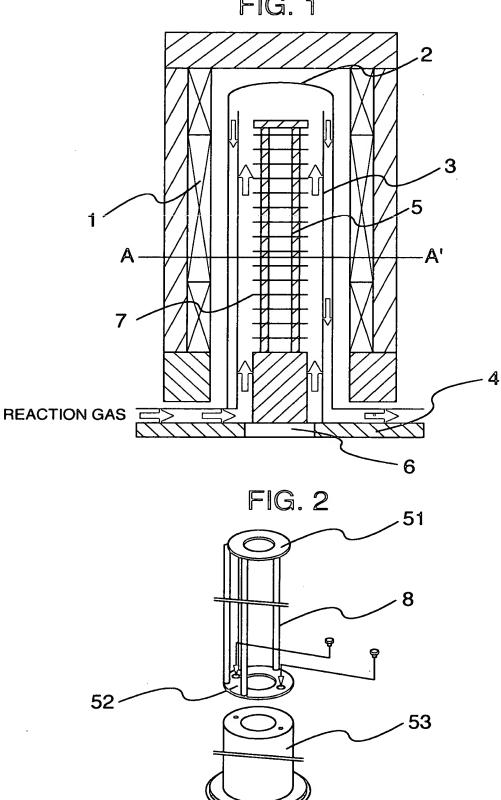
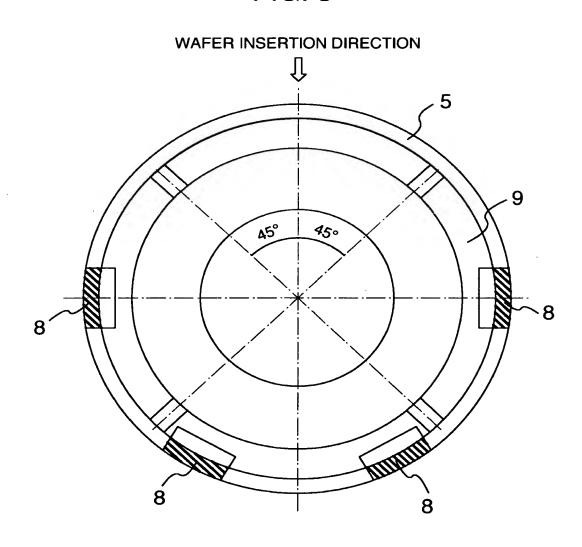


FIG. 3



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FIG. 4

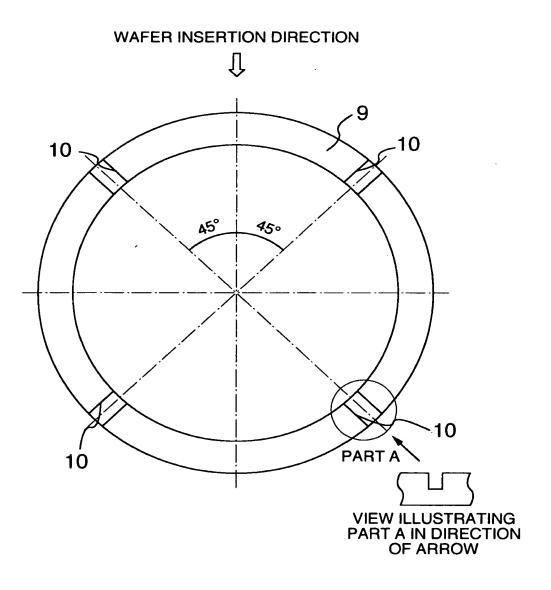
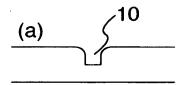
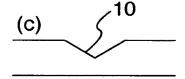
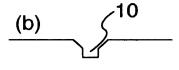
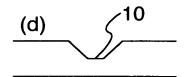


FIG. 5









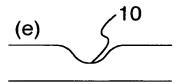


FIG. 6

# WAFER INSERTION DIRECTION $\iint$

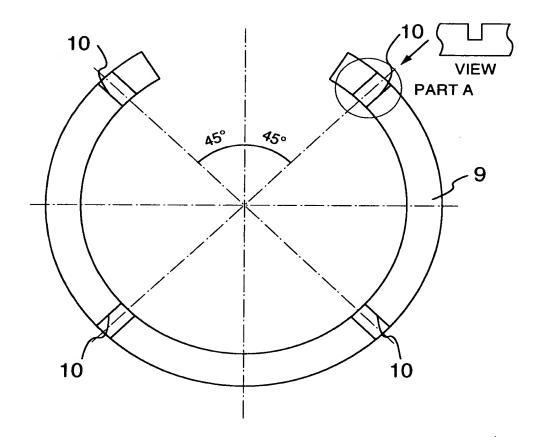
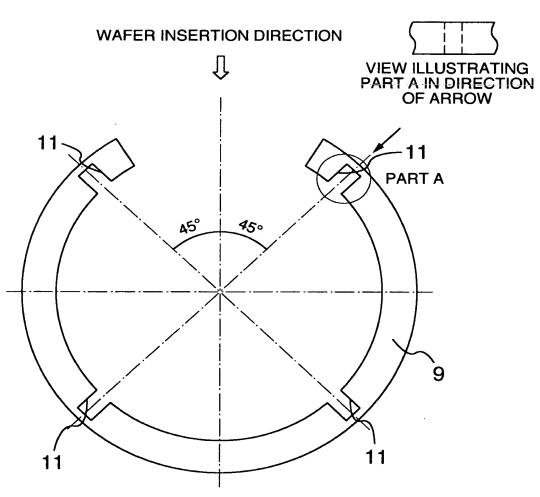


FIG. 7



### FIG. 8

#### WAFER INSERTION DIRECTION

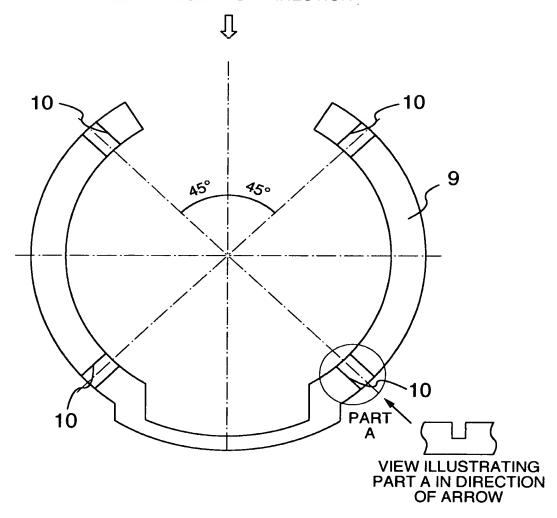


FIG. 9

## WAFER INSERTION DIRECTION

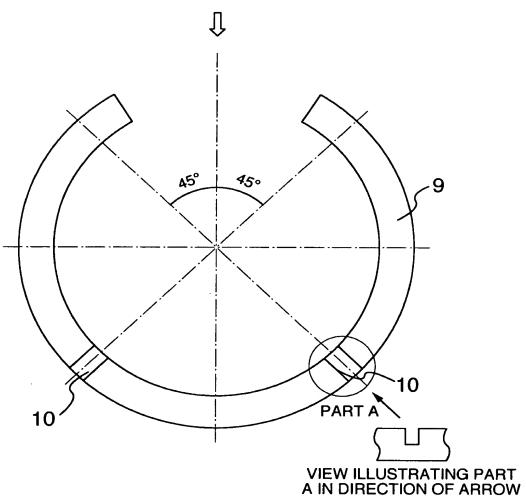


FIG. 10

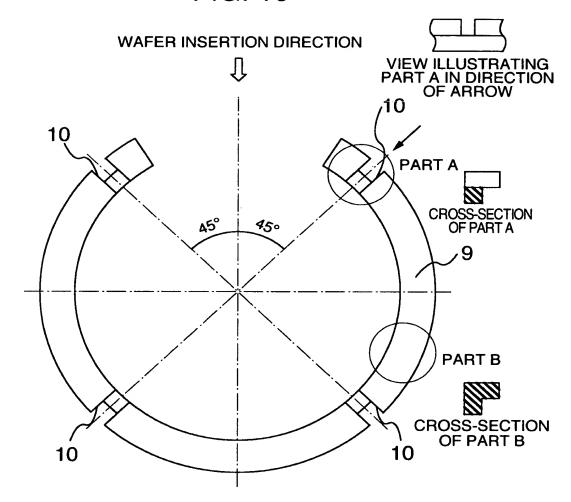


FIG. 11

#### WAFER INSERTION DIRECTION

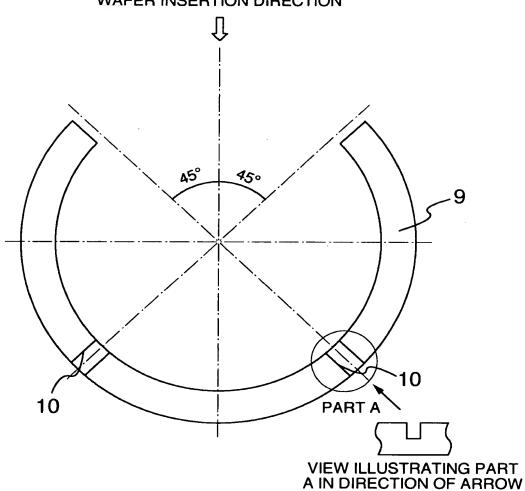
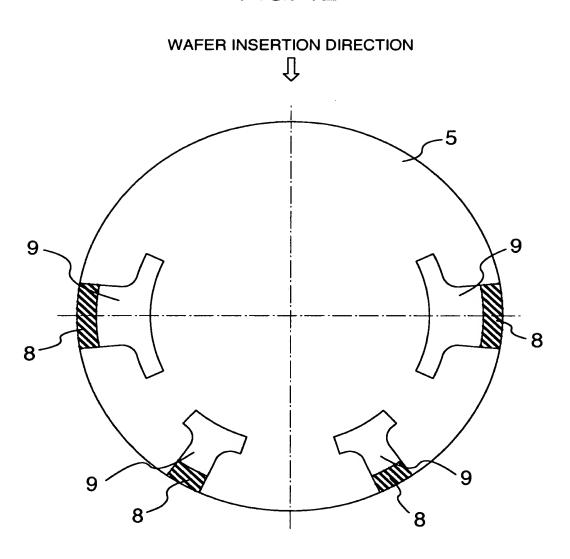


FIG. 12



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